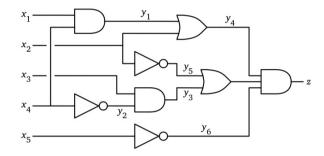
For mula Satisfinbility (SAT) Given a boolean formula Ø. Is there a way to set Is variable so it evals to true? SATENP: How d. I spt the variables? SAT is NP-hand (so SATE NP-complete

Reduce CircuitSAT to SAT.

Given a circuit K.

Assign each wire a variable. Write an equation describing each gate & D-c $(a \wedge b = c)$

A the equations & the variable E for the rateut

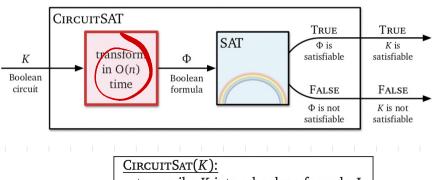


 $(y_1 = x_1 \land x_4) \land (y_2 = \overline{x_4}) \land (y_3 = x_3 \land y_2) \land (y_4 = y_1 \lor x_2) \land$ $(y_5 = \overline{x_2}) \land (y_6 = \overline{x_5}) \land (y_7 = y_3 \lor y_5) \land (z = y_4 \land y_7 \land y_6) \land z$

Lemma: K is sat. iff \$ is. =7: Usp K's good setting for x values. I set other variables by what their gates do.

All equations are sat, tzis set true, so \$ is true.

 \equiv Use \mathbf{P} 's setting of x values.



transcribe *K* into a boolean formula Φ return SAT(Φ) $\langle \langle \star \star \star MAGIC \star \star \star \rangle \rangle$

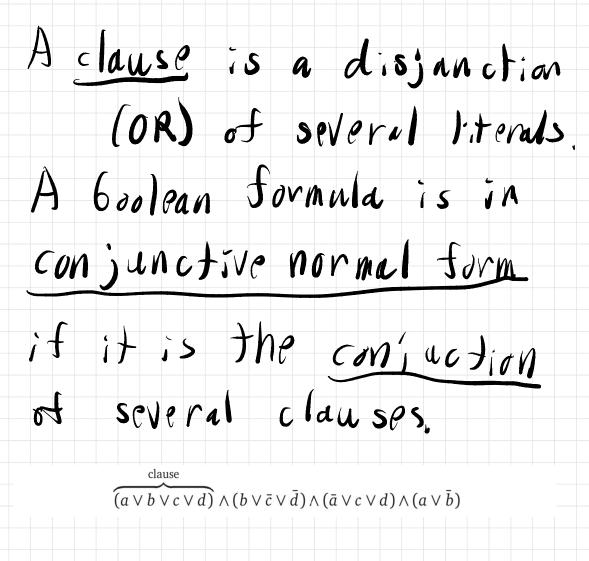
So we have poly time reduction Srom CircuitSAT to SAT.

So any problem AENP reduces to CircuitSAT which reduces to SAT. =7 SAT is NP-hard.

A literal is a bodlean

variable a or its

negation a.

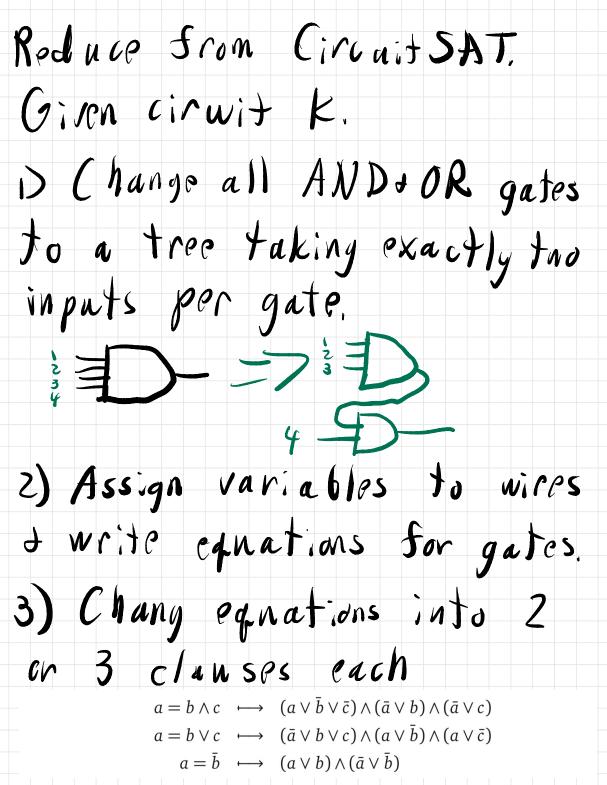


A <u>SCNF</u> formula is a <u>CNF</u> formula with <u>exactly</u> three literals per <u>clause</u>.

3SAT (ZCNF SAT): Given borlean formula in 3CNF. Can we set variables to make of true?

3SATENP.

3SATENP-complete.



9) Replace clauses with 1 or 2 literals with 4 on 2 clauses with 3 literals. (Use a new x dy for each of these transforms) $a \lor b \longmapsto (a \lor b \lor x) \land (a \lor b \lor \bar{x})$ $a \longmapsto (a \lor x \lor y) \land (a \lor \bar{x} \lor y) \land (a \lor x \lor \bar{y}) \land (a \lor \bar{x} \lor \bar{y})$ $(y_1 \lor \overline{x_1} \lor \overline{x_4}) \land (\overline{y_1} \lor x_1 \lor z_1) \land (\overline{y_1} \lor x_1 \lor \overline{z_1}) \land (\overline{y_1} \lor x_4 \lor z_2) \land (\overline{y_1} \lor x_4 \lor \overline{z_2})$ $\wedge (y_2 \lor x_4 \lor z_3) \land (y_2 \lor x_4 \lor \overline{z_3}) \land (\overline{y_2} \lor \overline{x_4} \lor z_4) \land (\overline{y_2} \lor \overline{x_4} \lor \overline{z_4})$ $\wedge (y_3 \lor \overline{x_3} \lor \overline{y_2}) \land (\overline{y_3} \lor x_3 \lor z_5) \land (\overline{y_3} \lor x_3 \lor \overline{z_5}) \land (\overline{y_3} \lor y_2 \lor z_6) \land (\overline{y_3} \lor y_2 \lor \overline{z_6})$ $\wedge (\overline{y_4} \lor y_1 \lor x_2) \land (y_4 \lor \overline{x_2} \lor z_7) \land (y_4 \lor \overline{x_2} \lor \overline{z_7}) \land (y_4 \lor \overline{y_1} \lor z_8) \land (y_4 \lor \overline{y_1} \lor \overline{z_8})$ $\wedge (y_5 \lor x_2 \lor z_9) \land (y_5 \lor x_2 \lor \overline{z_9}) \land (\overline{y_5} \lor \overline{x_2} \lor z_{10}) \land (\overline{y_5} \lor \overline{x_2} \lor \overline{z_{10}})$ $\wedge (y_6 \lor x_5 \lor z_{11}) \land (y_6 \lor x_5 \lor \overline{z_{11}}) \land (\overline{y_6} \lor \overline{x_5} \lor z_{12}) \land (\overline{y_6} \lor \overline{x_5} \lor \overline{z_{12}})$ $\wedge (\overline{y_7} \lor y_3 \lor y_5) \land (y_7 \lor \overline{y_3} \lor z_{13}) \land (y_7 \lor \overline{y_3} \lor \overline{z_{13}}) \land (y_7 \lor \overline{y_5} \lor z_{14}) \land (y_7 \lor \overline{y_5} \lor \overline{z_{14}})$ $\wedge (y_8 \vee \overline{y_4} \vee \overline{y_7}) \wedge (\overline{y_8} \vee y_4 \vee z_{15}) \wedge (\overline{y_8} \vee y_4 \vee \overline{z_{15}}) \wedge (\overline{y_8} \vee y_7 \vee z_{16}) \wedge (\overline{y_8} \vee y_7 \vee \overline{z_{16}})$ $\wedge (y_9 \vee \overline{y_8} \vee \overline{y_6}) \wedge (\overline{y_9} \vee y_8 \vee z_{17}) \wedge (\overline{y_9} \vee y_6 \vee z_{18}) \wedge (\overline{y_9} \vee y_6 \vee \overline{z_{18}}) \wedge (\overline{y_9} \vee y_8 \vee \overline{z_{17}})$ $\wedge (y_9 \lor z_{19} \lor z_{20}) \land (y_9 \lor \overline{z_{19}} \lor z_{20}) \land (y_9 \lor \overline{z_{19}} \lor \overline{z_{20}}) \land (y_9 \lor \overline{z_{19}} \lor \overline{z_{20}})$ Reduction takes O(n) Time. CircuitSAT True True 3SAT Φis K is transform Φ satisfiable satisfiable Κ in O(n)Boolean 3CNF

time

Boolean

formula

False

 Φ is not

satisfiable

False

K is not

satisfiable

circuit

So it's NP-hard.

Given undirected graph G = (V, E).

An independent set SEV

of G has no edge of 6

between its vertices

Max Ind Set: Find a max

size independent set.

Claim Max Ind Set is

NP-hard.

Roduce From 3SAT.

Given BCNF D. Baild a graph G.

kt # clause in I.

G gets 3k vertices, one per literal of T.

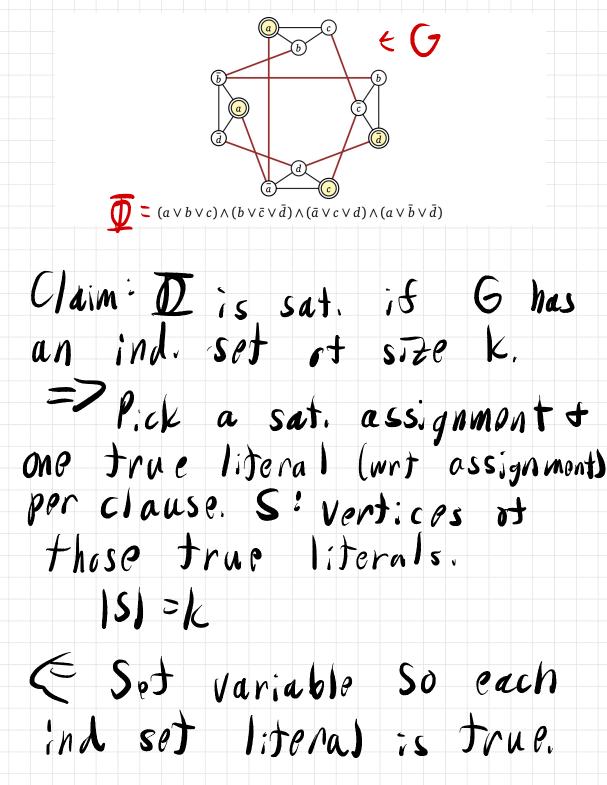
Any pair of literals in a

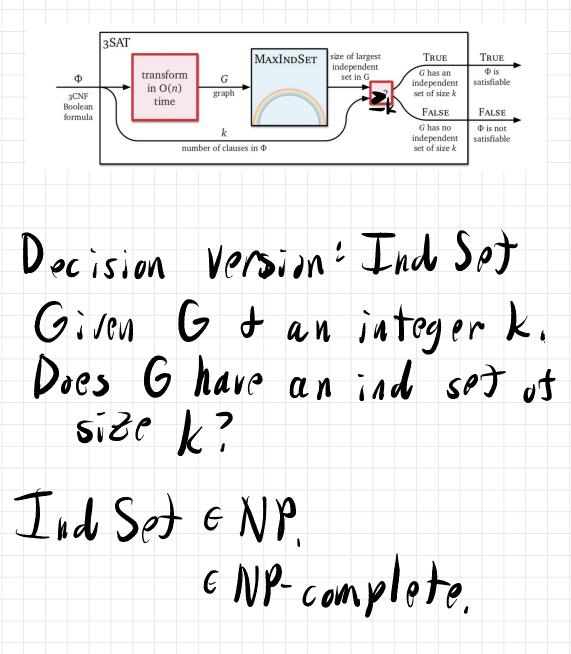
clause got a "triangle"

odge between their vertices.

Any two literals a ta

get a "negation" edge.



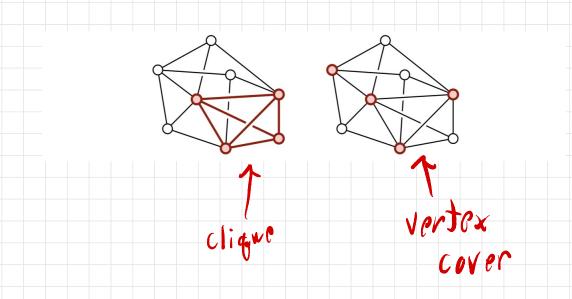


A clique is another name Sor a complete graph. Max Clique: Given G, what is the largest clipue Subgraph?

A vertex cover is a subset of vertices where each edge is hit at least once.

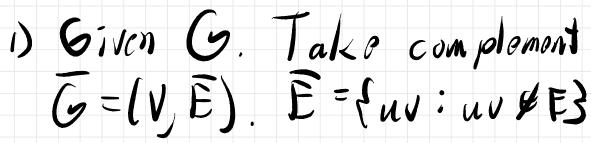
Min Vertex Cover : Find a min

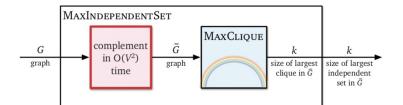
Size vertex cover.





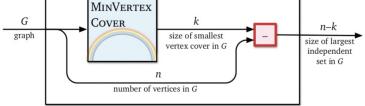






2) I is an ind, set of G = (V, E); sf $V \setminus I$ is a vertex cover,

MAXINDEPENDENTSET



Decision versions hard too => Clique & Vertex Cover ENP-complete