1. Consider the following different generations/types of operating systems.
   a. Multi-programming time sharing systems.
   b. Multi-programming batch systems.
   c. Pure batch systems.
   d. Personal computers that mainly focus on single users.
   e. An embedded computer that is used on your car to regulate the brake system. (It takes speed and brake pedal pressing level as input and brake pressure as output. The system has a simple CPU and some memory, but no disk.)
   f. A real time system that handles urgent events with different priorities. When a low priority task is in execution and some high priority tasks arrive, the system should switch to execute the one with the highest priority.

Which ones require an interrupt vector? For those that do require an interrupt vector, discuss what types of interrupts are required. For those that do not need an interrupt vector, discuss why not.

2. Consider the following pseudo assembly code for computing \( c = a + b \). Assume that \( a \), \( b \), and \( c \) are assigned to consecutive memory “words” (memory is generally addressed byte by byte and assume that a word is 4 bytes) and address for “a” is 0x0000e308. Also, we have \( a = 20 \), \( b = 136 \), and \( c = 0 \) at the starting time. Assume that the first instruction of the code is stored in 0x00001018. Also, each instruction has the opcode in the first byte (most significant byte) and the remaining 3 bytes specify the corresponding address. The opcode for load is 1, store is 2, and add is 3.

\[
\begin{align*}
\text{load } & a \\
\text{add } & b \\
\text{store } & c
\end{align*}
\]

(a) Show the memory addresses and contents for all the instructions and data involved. Use the format as follows to express your answer (but the following is not the answer). For all data, use hexadecimal representation.

<table>
<thead>
<tr>
<th>addresses</th>
<th>contents</th>
</tr>
</thead>
<tbody>
<tr>
<td>0x00002104</td>
<td>0x00000001</td>
</tr>
<tr>
<td>0x00002108</td>
<td>0x00000002</td>
</tr>
<tr>
<td>……</td>
<td>……</td>
</tr>
</tbody>
</table>

(b) Write the micro instructions for the code segment. Assume that current PC (program counter) is 0x00001018. For each micro-instruction, also indicate the data that is transferred (if it is a memory access). For all data, use the hexadecimal representation. The following are the first two micro-instructions and the data transferred. Complete the rest.

<table>
<thead>
<tr>
<th>Micro-instructions</th>
<th>data</th>
</tr>
</thead>
<tbody>
<tr>
<td>PC → MAR</td>
<td>0x00001018</td>
</tr>
<tr>
<td>M → MBR</td>
<td>0x0100e308</td>
</tr>
</tbody>
</table>

3. Assume that at time 5 no system resources are being used except for the processor and memory. Now consider the following events:

At time 5: \( P_1 \) executes a command to read from disk unit 3.
At time 15: \( P_5 \)'s time slice expires.
At time 18: P_7 executes a command to write to disk unit 3.
At time 20: P_3 executes a command to read from disk unit 2.
At time 24: P_5 executes a command to write to disk unit 3.
At time 28: P_5 is swapped out.
At time 33: An interrupt occurs from disk unit 2: P_3's read is complete.
At time 36: An interrupt occurs from disk unit 3: P_1's read is complete.
At time 38: P_5 terminates.
At time 40: An interrupt occurs from disk unit 3: P_5's write is complete.
At time 44: P_5 is swapped back in.
At time 48: An interrupt occurs from disk unit 3: P_7's write is complete.

For each time 22, 37, and 47, identify which state each process is in. If a process is blocked, further identify the event on which is it blocked.

4. Consider an assembly language with only four operations, load \( x \), store \( x \), add \( x \), and mul \( x \) (multiplication), where \( x \) can be a variable or a constant. The following code is in a pseudo high-level language. Translate the code into the assembly language.

\[
\text{int a, b, c = 0;} \\
A. \ a = 2; \\
B. \ b = 3; \\
C. \ c = a + b; \\
D. \ a = c * 5; 
\]

Indicate in the assembly code, at which points, there are potential of context switches.

5. Consider two functions P and Q, as shown in the following code. Note that each statement is labeled and the label is just for convenience purpose (no impact to execution).

\[
\text{int a, b = 0; } \\
\text{function P() } \\
\text{begin } \\
\text{A. a = a + 1;} \\
\text{B. b = a + b;} \\
\text{end;} \\
\text{function Q() } \\
\text{begin } \\
\text{D. b = 4;} \\
\text{E. a = a + b;} \\
\text{end;} \\
\text{main () … }
\]

(a) The main program is as follows. List all possible values of the pair \( (a, b) \) after the execution of the program.

\[
\text{main () } \\
\{ \text{int pid;} \\
\text{a = 1; pid = fork ();} \\
\text{if (pid == 0) } \{ \text{a = 2; P (); } \} \text{ else Q();} \\
\}
\]

(b) The pseudo code for the main program above is given in the following left. Assume that the program is compiled into executable “a.out”. And, “a.out” is invoked by the program at the following right. List all possible values of the pair \( (a, b) \) after the execution of the left program. Note: for this question, we assume that each statement is atomic, not each instruction.
6. Consider the following set of jobs.

<table>
<thead>
<tr>
<th>Job Number</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
<th>6</th>
</tr>
</thead>
<tbody>
<tr>
<td>Arrival Time</td>
<td>0</td>
<td>1</td>
<td>4</td>
<td>7</td>
<td>17</td>
<td>18</td>
</tr>
<tr>
<td>Service Time</td>
<td>6</td>
<td>2</td>
<td>4</td>
<td>2</td>
<td>4</td>
<td>2</td>
</tr>
</tbody>
</table>

Clearly show the scheduling of these jobs using the following algorithms (refer to the notes).
(a) Shortest job first (non-preemptive)
(b) Highest response ratio first (non-preemptive)
(c) Round-robin (time quantum = 1)
(d) Multilevel feedback queue (number of queue levels = 4, time quantum = 1)

Also, for each scheduling algorithm, compute (i) Average turnaround time, and (ii) Average (response time / service time).

7. Consider the MLFQ-like scheduling algorithm. Assume that the system has a time quantum 60ms. A process P enters the system, waits for 30ms, gets scheduled to run for 40ms and then blocks on I/O for 100ms. After finishes I/O, P returns to ready queue and waits for another 20ms and then gets scheduled to run for 20ms. What is the priority of P at this point?

8. Consider a real time system with the periodical jobs specified in the following table. A real time CPU scheduling algorithm, the rate monotonic algorithm, is used.

<table>
<thead>
<tr>
<th>Job name</th>
<th>A</th>
<th>B</th>
<th>C</th>
<th>D</th>
</tr>
</thead>
<tbody>
<tr>
<td>Period</td>
<td>6</td>
<td>4</td>
<td>12</td>
<td>6</td>
</tr>
<tr>
<td>Service time</td>
<td>1</td>
<td>1</td>
<td>2</td>
<td>1</td>
</tr>
</tbody>
</table>

(a) Clearly show the scheduling of the jobs.
(b) What is the throughput of the system?

9. Consider the program given in Question 4. Assume that A and B statements are in Thread X, and C and D statements are in Thread Y. A and B are executed sequentially, and C and D are executed sequentially. Threads X and Y execute concurrently. After execution of the program, what are the possible values of variables a, b, and c. Give all possible execution sequences and their results (of a, b, and c).

Hint: you need to consider interleaving between instructions.