Control structures practice problems

- branches and jumps are used to implement
 - conditional statements
 - loops
 - functions

More about functions

Leaf and non-leaf functions/procedures

- a leaf function does not call any other function or itself
- a non-leaf functions calls other functions or itself

If we call a function from within a function, \$ra will get overwritten. Problem!

Solution: save \$ra on the stack

We can also use the stack to store any variables we want.

the stack

the stack is a contiguous section of memory

the stack pointer (\$sp) points to the current top of the stack

when the stack is initialized, \$sp points to the bottom of available stack memory stack limit Garbage stack pointer bottom of stack

the stack grows upward

push and pop

PUSH copies a register to the stack

- used to save data on the stack

POP copies a value from the stack to a register

- used to retrieve data from the stack

Many ISAs have PUSH and POP instructions

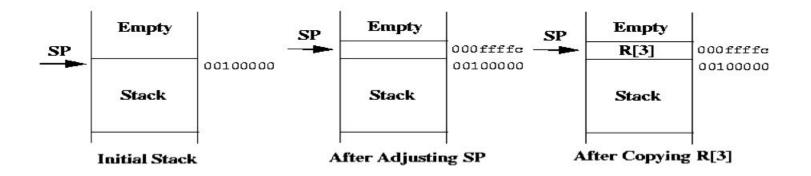
MIPS uses load and store instructions

push

This is how we push:

addi \$sp, \$sp, -4

sw \$s3, (\$sp)



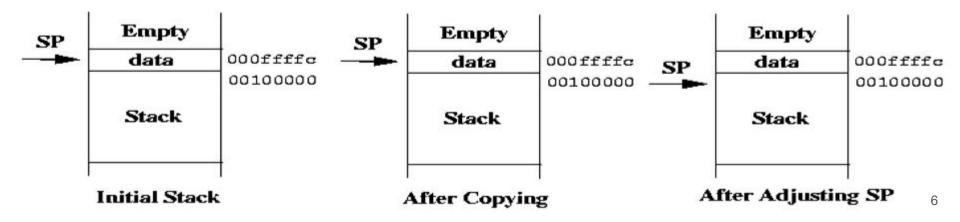
pop

This is how we pop:

lw \$s3, (\$sp)

addi \$sp, \$sp, 4

Notice that the data is still there in the now unused portion of the stack.



stacks and functions

In some architectures, each time a function is called, the entire state of the machine (registers) is stored on the stack

If you have recursive calls, you can run out of stack space - stack overflow!

MIPS reduces the likelihood of this by providing register conventions of what is saved across function calls

You need to use the stack in MIPS for non-leaf functions and if you want to pass in more than 4 arguments or pass out more than 2 arguments

leaf function example

Args in \$a registers

f in \$s0;

result in \$v0

C Code:

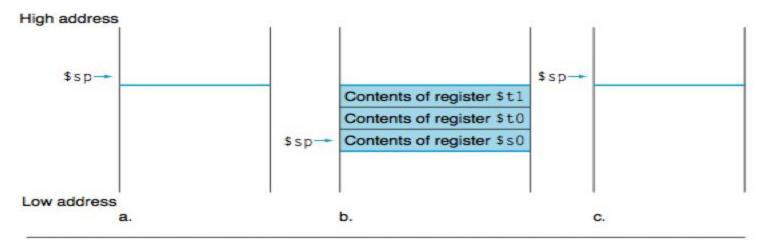
```
int leaf (int g, h, i, j)
{
    int f;
    f = (g + h) - (i + j)
    return f;
```

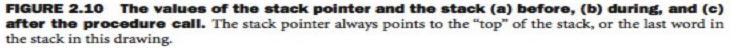
<u>e:</u>	
\$ <u>sp</u> , −4	
0(\$sp)	Save
\$a0, \$a1	
\$a2, \$a3	Proc
\$t0, \$t1	
\$s0, \$zero	Res
0(\$sp)	Rest
\$sp, 4	Resi
	Retu
	<pre>\$sp, -4 0(\$sp) \$a0, \$a1 \$a2, \$a3 \$t0, \$t1 \$s0, \$zero 0(\$sp)</pre>

Save \$s0 on stack Procedure body Result Restore \$s0 Return

1	# lost	function	with	2 FOURO	atc an	d nur	h/non													
102102		tion_leaf			its an	u pus	n/pop													
2					a and		isters, h	au ta u	co stack											
3	# purp	ise: demo	now	to use	a anu	v reg	isters, n	ow to u	Se Stack											
5		.data																		
6	f:	.word	0																	
7	g:	.word	5																	
8	h:	.word	12																	
	//. i:	.word	4																	
9			3																	
10	j:	.word	2																	
11																				
12		.text	t=0			1														
13		lw	\$a0,		#	Load	argument	S												
14		lw	\$a1,																	
15		lw	\$a2,																	
16		lw	\$a3,	j																
17																				
18		li	\$s0,							purposes 30	calc:	# retu))				
19							to demon			31		# gj								
20			regis	ters ar	e not	prese	rved in a	functi	on call	32		# resu	lt is	retur	ned in	1 \$V0				
21		li	\$t0,	9						33										
22		li	\$t1,	9						34		# push	\$50 0	n sta	ck					
23										35		addi	\$sp,	\$sp,	-4					
24		jal	calc							36		SW	\$s0,	(\$sp)					
25		SW	\$v0,	f	#	save	result f	rom fun	ction call	37										
26										38		add	\$t0,	\$a0,	\$a1					
27	exit:	li	\$v0,	10						39		add		\$a2,						
28	1999-1999	syscall								40		sub		\$t0,		# W	e ci	ould h	have	used
20										41		add			\$zero					
										42					1000					
										43		# pop	ss0 fr	om st	ack					
										45		lw		(\$sp						
										45		addi		\$sp						
										45		# retu		4241						9
										47		jr	\$ra							

saving multiple registers on the stack





\$t and \$s registers

The following convention is used in MIPS:

- \$t0 \$t9 are temporary registers that the called function is not required to save
- \$s0 \$s7 are saved registers that the called function must preserve them by saving/restoring on the stack

This is a convention:

- not enforced by the assembler
- agreed upon by programmers to make code easier to read

byte operations

lb rt, offset(rs) # load a byte from address rs+offset to rt (sign extends)

Ibu rt, offset(rs) # load a byte from address rs+offset to rt (no sign extend)

sb rt, offset(rs) # store register rt at address rs+offset

string copy example

C code:

```
void strcpy (char x[], char y[])
{
    int i = 0;
    while ((x[i]=y[i] != '\0')
        i ++ 1;
}
```

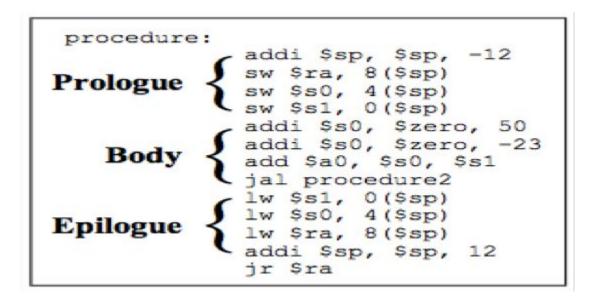
MIPS code:

stro	cpy:				
	addi	\$sp,	\$sp, -4	#	adjust stack for 1 item
	SW	\$s0,	0(\$sp)		save \$s0
	add	\$s0,	\$zero, \$zero	#	$\mathbf{i} = 0$
L1:	add	\$t1,	\$s0, \$a1	#	addr of y[i] in \$t1
	lbu	\$t2,	0(\$t1)		$t_{2} = y[i]$
	add	\$t3,	\$s0, \$a0	#	addr of x[i] in \$t3
	sb	\$t2,	0(\$t3)	#	x[i] = y[i]
	beq	\$t2,	\$zero, L2	#	<pre>exit loop if y[i] == 0</pre>
	addi	\$s0,	\$s0, 1	#	i = i + 1
	j	L1		#	next iteration of loop
L2:	lw	\$s0,	0(\$sp)	#	restore saved \$s0
	addi	\$sp.	\$sp, 4	#	pop 1 item from stack
	jr	\$ra			and return

_

```
# string copy example
 1
 2
    # C equivalent - copies x to y
            void strcpy (char x[], char y[]) {
 з
    #
    #
            int i = 0;
 4
                while ((y[i] = x[i]) != '(0))
 5
    #
    #
                    1 += 1;
 6
    #
 7
 8
 9
    .data
10
    string1:
                    .asciiz "Hello"
11
12
    string2:
                    .byte 6
13
14
    .text
15
16
    main:
17
            add
                    $s0, Szero, Szero
                                                     # s\theta = i
            la
                    $s1, string1
18
                    $s2, string2
19
            la
20
    L1:
            add
                    $t1, $s1, $s0 # t1 = address of x[i]
21
            lbu
                    st2, 0(st1)
                                   # t2 = x[i]
                                   # t3 = address of y[i]
22
            add
                    st3, ss2, ss0
23
            sb
                    st2, 0(st3)
                                     # y[i] = x[i]
                    St2, Szero, exit
24
            beq
25
                    $$0, $$0, 1
            addi
26
                    L1
            j
27
28
    exit:
29
            li
                    Sv0, 10
30
            syscall
31
```

form of a function



non-leaf functions

C code:

```
int fact (int n)
{
    if (n < 1) return f;
    else return n * fact(n-1);</pre>
```

MIPS code:

```
fact:
    addi $sp, $sp, -8
                          # adjust stack for 2 items
         $ra, 4($sp)
                          # save return address
    SW
         $a0, 0($sp)
                          # save argument
    SW
    slti $t0, $a0, 1
                          # test for n < 1
         $t0, $zero, L1
    bea
    addi $v0, $zero, 1
                          # if so, result is 1
                              pop 2 items from stack
    addi $sp, $sp, 8
                          #
         $ra
                               and return
    jr
L1: addi $a0, $a0, -1
                          # else decrement n
    ial
                          # recursive call
         fact
         $a0, 0($sp)
                          # restore original n
    ]w
         $ra, 4($sp)
                          #
                              and return address
    ]w
    addi $sp, $sp, 8
                          # pop 2 items from stack
    mul
         $v0, $a0, $v0
                          # multiply to get result
    ir
                          # and return
         $ra
```

```
# nonleaf example 1
 1
     # find factoral n!
 2
     # C implementation
 3
             int fact (int n) {
 4
     #
                     if (n < 1) return (1);
 5
     #
 6
                     else return (n * fact(n-1);
     #
 7
     #
             3
 8
 9
     .data
10
    n:
             .word
                     з
11
    nfact:
             .word
                     0
12
13
     .text
             # load the data
    main:
14
15
             lw
                      $s0, nfact
             lw
                     $s1, n
16
17
             add
                     $a0, $s1, $zero # copy data to arguments
18
19
                              # call subroutine to find factorial
             jal
                      fact
20
                     $v0, nfact
21
             SW
22
             j
23
                     exit
24
             # subroutine to find the factorial of n
25
     fact:
26
```

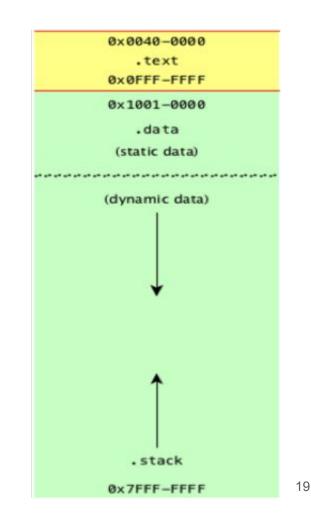
```
# subroutine to find the factorial of n
    fact:
25
26
27
           # save registers on the stack: $a0 and $ra
28
           addi
                   $sp, $sp, -8
                   $ra, 4($sp)
29
           SW
30
                   $a0, 0($sp)
           SW
31
32
           # factorial
33
           slti $t0, $a0, 1
                                # test for n < 1
34
           bea
                   $t0, $zero, L1
35
36
           # base case n =0
37
           addi
                   $v0, $zero, 1
                                  # return 1
           addi
                   $sp, $sp, 8 # pop 2 items off stack
38
                                  # return to called
39
           jr
                   $ra
40
41
   L1:
           addi
                   $a0, $a0, -1 # decrement n
42
           jal
                   fact
                                  # call fact again
43
44
           # restore variables off the stack: pop
45
                   $a0, 0($sp) # restore n
           lw
                   $ra, 4($sp) # restore return address
46
           lw
47
           addi
                   $sp, $sp, 8
                                  # adjust sp
48
49
           # multiply and exit
50
           mul $v0, $a0, $v0 # return n * fact(n-1)
51
           ir
                   $ra
           # end of calc
52
53
54
   exit:
55
           li $v0, 10
           syscall
56
```

MIPS static and dynamic variables

Data in the .data section is static data.

We can also allocate data at run time on the heap.

Notice that the stack and the heap grow towards each other.



memory management

In C, programmers allocate dynamic memory with malloc() and free it with the free() function.

A memory leak occurs if a program does not release the bytes on the heap when it is finished with it.

Another source of errors is releasing the memory before the program is finished with them.

These types of problems led to the development of Java and C++

arrays v. pointers: clearing an array

int for	(int array[], int i; (i = 0; i < size; ray[i] = 0;		<pre>clear2(int *array, int size) { int *p; for (p = &array[0]; p < &array[size]; p = p + 1) *p = 0; }</pre>					
	move \$t0,\$zero	# i = 0	<pre>move \$t0,\$a0 # p = & array[0]</pre>					
loop1:	sll \$t1,\$t0,2	# \$t1 = i * 4	<pre>sll \$t1,\$a1,2 # \$t1 = size * 4</pre>					
	add \$t2,\$a0,\$t1	# \$t2 =	add \$t2,\$a0,\$t1 # \$t2 =					
		# &array[i]	# &array[size]					
	sw \$zero, 0(\$t2)	<pre># array[i] = 0</pre>	loop2: <u>sw</u> \$zero,0(\$t0) # Memory[p] = 0					
	addi \$t0,\$t0,1	# i = i + 1	addi $t0,t0,4 \# p = p + 4$					
	slt \$t3,\$t0,\$a1	# \$t3 =	<pre>slt \$t3,\$t0,\$t2 # \$t3 =</pre>					
		# (i < size)	#(p<&array[size])					
	bne \$t3,\$zero,lo	op1 # if ()	bne \$t3,\$zero,loop2 # if ()					
		# goto loop1	# goto loop2					

arrays v. pointers

Array indexing involves:

- multiplying index by element size
- added to array base address

Pointers correspond directly to memory addresses.

Can avoid indexing complexity