Electrical Engineering Colloquium
Dallas Chapter of IEEE Signal Processing Society Presents

"Multiple-Input Multiple-Output Antenna Processing For Broadband"

Dr. Srinath Hosur
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Room ECSN 2.126
Friday, April 16, 2004
11:00 am

This talk presents some on going work in the area of multi-antenna wireless transceivers enhanced with multiple-input multiple-output (MIMO) antenna processing for increased rate and reach. While multi-antenna techniques have existed for many years in complex and expensive communication systems, increased computational capabilities at a low cost are now making MIMO attractive for consumer devices. This talk focuses on some MIMO techniques coupled with Orthogonal Frequency-Division Multiplexing. We also discuss a rapid prototyping platform used for analysis and some field experiments of a MIMO OFDM system.

Dr. Srinath Hosur received his PhD degree in electrical engineering from the University of Minnesota in 1996. He joined the DSP Solutions R&D Center of Texas Instruments in 1995 where he worked on PHS chipset development. From 1997 to 2000, he worked on WCDMA and helped TI influence the WCDMA standards in the areas of synchronization and transmit antenna diversity. In 2000, he helped TI establish a fixed-wireless access chipset business, where he was the lead for PHY algorithm development and modem bring up. From 2002, Dr. Hosur has been the algorithm lead for the Wireless Broadband Architectures group. He is currently leading the Emerging Broadband Wireless Communications group, where he is a senior member of technical staff.

Refer to http://www.utdallas.edu/~kehtar/ieee-sp/ieee-sp.index.html for more information on the Dallas Chapter of the IEEE Signal Processing Society.