

Performance Robustness Analysis of VLSI Circuits with Process Variations Based on Kharitonov's Theorem^{*}

Liuxi Qian^{*1} Dian Zhou¹, Sheng-Guo Wang² and Xuan, Zeng³

¹Erik Jonsson School of Engineering and Computer Science, University of Texas at Dallas, Dallas, TX 75080, USA)

²College of Engineering, University of North Carolina at Charlotte, Charlotte, NC 28223-0001, USA)

³Department of Microelectronics, Fudan University, Shanghai 201203, China)

* Email: liuxi85q@gmail.com

Abstract— In today's VLSI technology, the process variations are unavoidable. This paper proposes an efficient analysis approach for exploring the worst case performance for VLSI circuits with severe parameter value variations due to nano-scale process. Inspired by Kharitonov's theorem, the described method dramatically reduces the computational burden to only evaluate several critical Kharitonov-type interval transfer functions. The computational efficiency of the method is demonstrated by two practical VLSI circuits.

I. INTRODUCTION

As the advanced VLSI technology has reached 22nm feature size and below, process lithography no longer produces the ideal dimension of circuit components. The corresponding electrical parameters may vary as large as 1/3 or more [1]. Therefore, today's computer-aided design (CAD) tools are not only applied to design nominal VLSI circuits to meet the specs, but also assist in performance robustness analysis of nano-scale VLSI circuits with the effect from the unavoidable manufactured fluctuations. Current CAD tools to deal with performance robustness analysis of VLSI circuits with process variations are mainly developed along the line of the Monte Carlo method or similar stochastic and statistical analysis methods, e.g., [2], [3]. However, they all require long simulation time and underestimate the likely performance variation range when the sampling numbers are not sufficient enough. A main concern is to determine the worst case performance range of nano-scale VLSI circuits without evaluation of large number of samples as required by Monte Carlo method.

In this paper, we interest in calculating the worst case frequency-domain performance of VLSI circuits with manufactured variations. The uncertainties of electrical parameters are represented as real interval numbers. Given an uncertain electrical parameter p , suppose there is a variation level of $\pm 30\%$ to it, then p varies within an interval of $[p^-, p^+]$. p^- and p^+ are the endpoints of the interval, and p^- equals to $p \times (1-30\%)$, while p^+ equals to $p \times (1+30\%)$. Note that it is not necessary to know the statistical properties of p but the endpoints of the interval. Consider the behavior of a VLSI circuit is governed by an interval transfer function:

$$H(s; a, b) = \frac{N(s, a)}{D(s, b)} = \frac{a_0 + a_1s + \dots + a_ms^m}{b_0 + b_1s + \dots + b_ns^n} \quad (1)$$

where a_i ($i=0, 1, \dots, m$) and b_i ($i=0, 1, \dots, n$) are uncertain coefficients for numerator and denominator. They vary randomly within $[a_i^-, a_i^+]$ and $[b_i^-, b_i^+]$ respectively. In practical VLSI circuits, a_i and b_i are usually constituted of several underlying parameters. Therefore, the variation ranges of coefficients should be converted from their corresponding parameters' variation ranges by applying interval arithmetic [4].

The rest of this paper is organized as follows. Section II briefly explains the geometrical properties of Kharitonov's theorem. Section III introduces critical transfer functions and their construction. The application of the proposed idea to two practical VLSI circuits is described in section IV. Finally, section V concludes this paper.

II. KHARITONOV'S THEOREM AND ITS GEOMETRICAL PROPERTIES

Kharitonov's Theorem [5] is a seminal theorem to determine robust stability of perturbed control systems. Given a perturbed control system which is modeled by a family of real interval polynomials:

$$p(s) = q_0 + q_1s + \dots + q_ms^m, q \in Q \quad (2)$$

where q_i is uncertain coefficient with variation range $[q_i^-, q_i^+]$. Kharitonov's theorem states that the Hurwitz stability of a family of real interval polynomials can be guaranteed by the Hurwitz stability of four prescribed critical vertex polynomials (Kharitonov's Polynomials) in this family which are given as follows:

$$K_1(s) = P_{el}(s) + P_{ol}(s) = (q_0^- + q_2^+s^2 + \dots) + (q_1^-s + q_3^+s^3 + \dots) \quad (3a)$$

$$K_2(s) = P_{el}(s) + P_{oh}(s) = (q_0^- + q_2^+s^2 + \dots) + (q_1^+s + q_3^-s^3 + \dots) \quad (3b)$$

$$K_3(s) = P_{eh}(s) + P_{ol}(s) = (q_0^+ + q_2^-s^2 + \dots) + (q_1^-s + q_3^+s^3 + \dots) \quad (3c)$$

$$K_4(s) = P_{eh}(s) + P_{oh}(s) = (q_0^+ + q_2^-s^2 + \dots) + (q_1^+s + q_3^-s^3 + \dots) \quad (3d)$$

where P_{el} , P_{eh} (P_{ol} , P_{oh}) are responding to smallest and largest even (odd) part. For clearly understanding the Kharitonov's theorem, we search its geometrical interpretation in frequency-domain through substituting $s=j\omega$ in (2). For a fixed frequency $\omega=\omega_0$, the image of (2) in complex plane is in shape of rectangle (Kharitonov's rectangle) as illustrated in Fig.1 (a) and its four vertices are corresponding to four Kharitonov's polynomials. We denote $|P(j\omega)|$ the distance from the origin to a point within the Kharitonov's rectangle, while $\arg P(j\omega)$ stands for the angle between the positive Re -axis and the line from the origin to a

^{*}This work is partially supported by Semiconductor Research Corporation (SRC) grant 637163

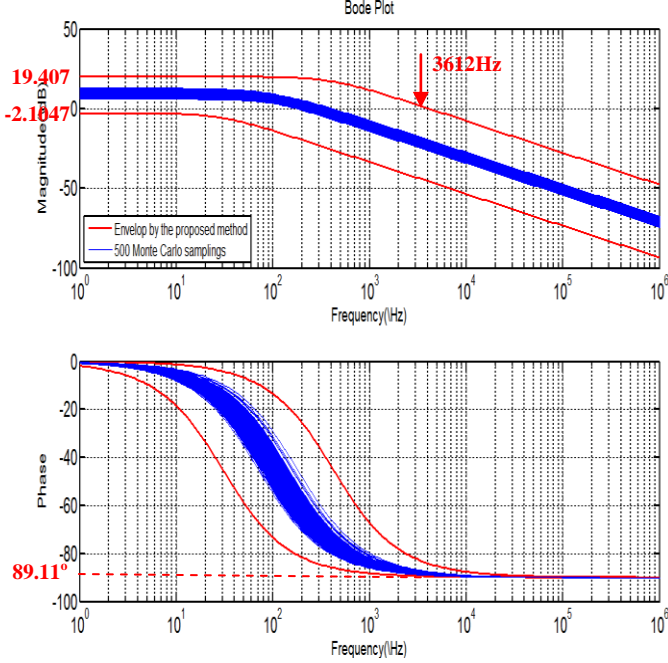


Fig. 4 Simulation result of common source amplifier (envelopes by the proposed method and 500 Monte Carlo samplings)

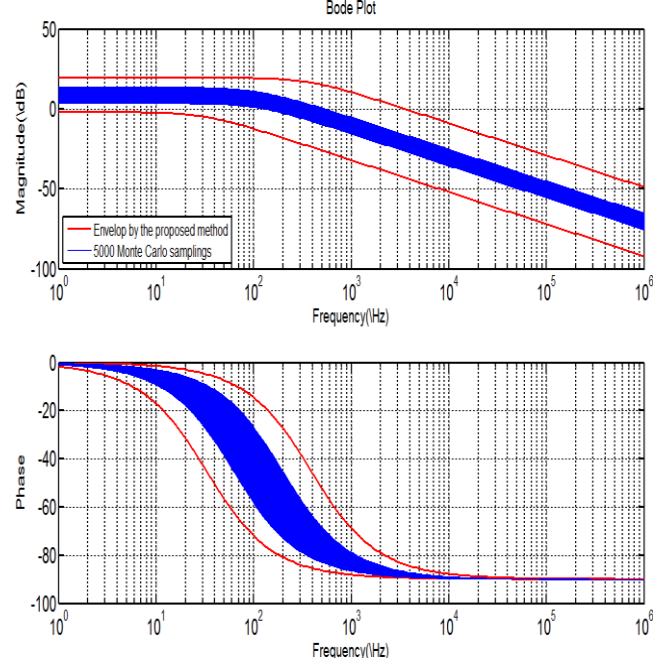


Fig.5 Simulation result of common source amplifier (envelopes by the proposed method and 5000 Monte Carlo samplings)

IV. CASE STUDIES

To illustrate the basic idea of our approach, two cases are studied. One is a common source amplifier, another one is Wien Bridge oscillator.

Case 1. Consider a common source amplifier of Fig.3 (a), its transfer function of output/input voltage is given:

$$\frac{V_{out}(s)}{V_{in}}(s) = \frac{(C_{GD}s - g_m)R_D'}{R_S R_D' \zeta s^2 + [\zeta + R_S C_{GS} + R_D'(C_{GD} + C_{DB} + C_L)]s + 1} \quad (7)$$

Where $\zeta = C_{GS}C_{GD} + C_{GS}(C_{DB} + C_L) + (C_{DB} + C_L)C_{GD}$, $R_D' = R_D/r_o$, $\zeta = R_S C_{GD}(1 + g_m R_D)$. Assume that there is $\pm 30\%$ variation to every parameter. The nominal value and tolerance range of each electrical parameter is listed in Table. 1. The variation ranges of parameters are firstly mapped into coefficients' variation ranges by applying interval arithmetic. Given two real interval numbers p_1 and p_2 , then the basic interval arithmetic operations are:

- 1) $p_1 + p_2 = [p_1^- + p_2^-, p_1^+ + p_2^+]$;
- 2) $p_1 - p_2 = [p_1^- - p_2^+, p_1^+ - p_2^-]$;
- 3) $p_1 \times p_2 = [\min(p_1^- p_2^-, p_1^- p_2^+, p_1^+ p_2^-, p_1^+ p_2^+), \max(p_1^- p_2^-, p_1^- p_2^+, p_1^+ p_2^-, p_1^+ p_2^+)]$;
- 4) $p_1/p_2 = p_1 \times (1/p_2)$; $p_2 \neq 0$

For example, the first-order coefficient of numerator of (7) is $C_{GD} \times R_D'$, the corresponding variation range of a_1 is the product of C_{GD} and R_D' , that is $[0.0357 \times 10^{10}, 0.4253 \times 10^{10}]$. The remaining coefficients' variation ranges are $[-9.3401, -0.7851]$ for a_0 , $[0.0025, 0.0307]$ for b_1 , and $[0.0175 \times 10^{-12}, 0.7187 \times 10^{-12}]$ for b_2 , respectively. Secondly, the eight Kharitonov's polynomials for numerator and denominator are calculated in equation (8a) to (8h):

$$N_1(s) = N_{ei}(s) + N_{oi}(s) = -9.3401 + 0.0357 \times 10^{-10}s \quad (8a)$$

$$N_2(s) = N_{ei}(s) + N_{oh}(s) = -9.3401 + 0.4253 \times 10^{-10}s \quad (8b)$$

$$N_3(s) = N_{eh}(s) + N_{oi}(s) = -0.7851 + 0.0357 \times 10^{-10}s \quad (8c)$$

$$N_4(s) = N_{eh}(s) + N_{oh}(s) = -0.7851 + 0.4253 \times 10^{-10}s \quad (8d)$$

$$D_1(s) = D_{ei}(s) + D_{oi}(s) = 1 + 0.7187 \times 10^{-12}s^2 + 0.0025s \quad (8e)$$

$$D_2(s) = D_{ei}(s) + D_{oh}(s) = 1 + 0.7187 \times 10^{-12}s^2 + 0.0307s \quad (8f)$$

$$D_3(s) = D_{eh}(s) + D_{oi}(s) = 1 + 0.0175 \times 10^{-12}s^2 + 0.0025s \quad (8g)$$

$$D_4(s) = D_{eh}(s) + D_{oh}(s) = 1 + 0.0175 \times 10^{-12}s^2 + 0.0307s \quad (8h)$$

Following corollary 3.1 and corollary 3.2, 48 and 16 critical Kharitonov-type transfer functions are readily constructed from (8a) to (8h). The calculated worst case variation ranges of magnitude and phase response are plotted in red lines in Fig.4 and Fig.5. We compare the envelopes with 500 Monte Carlo simulation samples in Fig.4 and 5000 Monte Carlo samplings in Fig.5, respectively. Their parameters are randomly sampled from corresponding variation ranges shown in Table.1. It is clearly illustrated that the Monte Carlo samplings are well enclosed by the envelopes. The more Monte Carlo samplings are been sampled, the closer they are to the envelopes, and they will finally hit the envelopes if sufficient samplings are simulated. From the calculated envelopes, we can tell that because of the process variations, the gain of the common source amplifier may be as high as 19.407dB, the highest UGF may be 3612Hz, and the highest phase margin may be 89.1°. The envelopes also predict that the common source amplifier may lose the ability of amplification, since the lowest gain may be -2.1047dB. It provides an useful information to designers to optimize their circuit by considering likely performance under process variations.

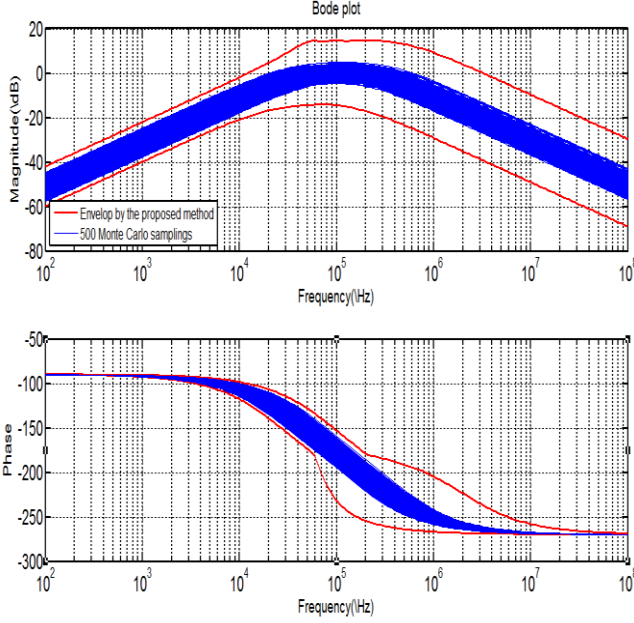


Fig.6 Simulation result of Wien Bridge oscillator (envelopes by the proposed method and 500 Monte Carlo samplings)

Case 2. Consider a Wien Bridge oscillator shown in Fig.3 (b). The transfer function is given as:

$$\frac{V_o}{V'_o}(s) = \frac{(1 + \frac{R_F}{R_S})R_1C_2s}{R_1R_2C_1C_2s^2 + (R_1C_1 + R_2C_2 + R_1C_2)s + 1} \quad (9)$$

With assumption of $\pm 30\%$ variation level to every parameter, Table.2 lists the nominal value and variation range of every parameter. A similar transformation from variation ranges of parameters to the coefficients' variation ranges is applied at first, and they are $[1.0177 \times 10^{-5}, 7.9672 \times 10^{-5}]$ for a_1 , $[1.47 \times 10^{-5}, 5.07 \times 10^{-5}]$ for b_1 , $[2.401 \times 10^{-11}, 2.8561 \times 10^{-10}]$ for b_2 . Next, the eight Kharitonov's polynomials for numerator and denominator can be formulated as:

$$N_{o1}(s) = 1.0177 \times 10^{-5} s \quad (10a)$$

$$N_{o2}(s) = 7.9672 \times 10^{-5} s \quad (10b)$$

$$D_1(s) = D_{e1}(s) + D_{o1}(s) = 1 + 2.8561 \times 10^{-10} s^2 + 1.47 \times 10^{-5} s \quad (10c)$$

$$D_2(s) = D_{e2}(s) + D_{o2}(s) = 1 + 2.8561 \times 10^{-10} s^2 + 5.07 \times 10^{-5} s \quad (10d)$$

$$D_3(s) = D_{e3}(s) + D_{o3}(s) = 1 + 2.401 \times 10^{-11} s^2 + 1.47 \times 10^{-5} s \quad (10e)$$

$$D_4(s) = D_{e4}(s) + D_{o4}(s) = 1 + 2.401 \times 10^{-11} s^2 + 5.07 \times 10^{-5} s \quad (10f)$$

Since there is only one item in the numerator of (9), the numbers of critical Kharitonov-type transfer functions are reduced to 20 for magnitude response and 8 for phase response. Fig.6 and Fig.7 display the calculated worst case performance ranges of magnitude and phase responses of Wien Bridge oscillator in red lines, and 500 Monte Carlo samplings and 5000 Monte Carlo samplings in blue lines. The more samplings are simulated, the closer they are to the envelopes. The envelopes predict that the frequency which may cause oscillation varies from 1.8834 Hz to 6.4961 Hz, when the Monte Carlo samples fail to evaluate the likely performance range for Wien bridge oscillator within finite samplings.

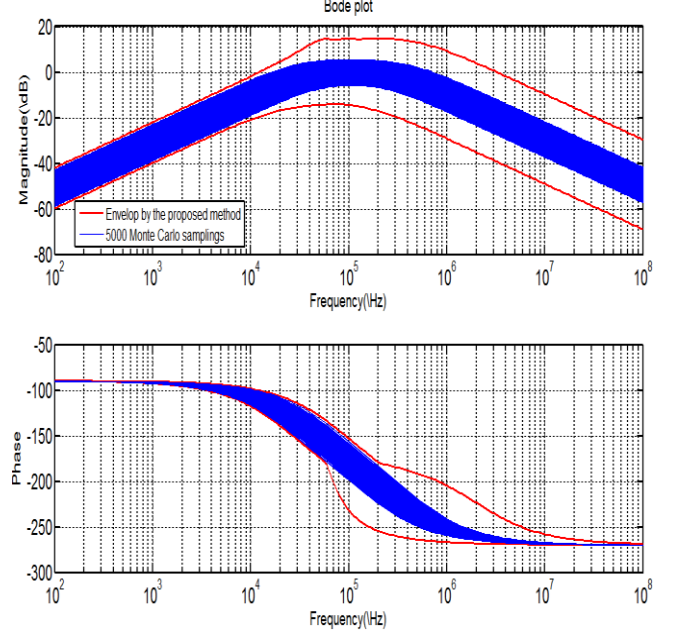


Fig.7 Simulation result of Wien Bridge oscillator (envelopes by the proposed method and 5000 Monte Carlo samplings)

V. CONCLUSION

Two practical VLSI circuits are studied to demonstrate the efficiency of the proposed approach in performance robustness analysis of VLSI circuits with process variations. This method takes advantage of Kharitonov's theorem and reduces the computational complexity to at most 48 Kharitonov-type critical transfer functions' calculation even for the complicated VLSI circuits with large transfer function order. The approach provides a good graphic means of investigating of worst case frequency-domain performance of VLSI circuits with manufactured variations. The proposed method is general and can be extended to evaluate worst case performance for other circuits and other performance metrics of interest when a similar interval transfer function is established.

REFERENCES

- [1] X. Li, J. Le, P. Gopalakrishnan, and L. Pileggi, "Asymptotic probability extraction for non-normal distributions of circuit performance," in ICCAD'04, pp. 2-9, 2004.
- [2] P. Ghanta, S. Vrudhula, R. Panda and J. Wang, "Stochastic power grid analysis considering process variations," Proc. DATE '05, pp. 964-969, 2005.
- [3] P. Heydari and M. Pedram, "Model-order reduction using variational balanced truncation with spectral shaping," IEEE Trans. CAS-I, Vol.53, no.4, pp.879-891, 2006.
- [4] R. E. Moore, *Interval analysis*. Englewood Cliffs, NJ: Prentice-Hall, 1966.
- [5] V. L. Kharitonov, "Asymptotic stability of an equilibrium of a family of system of linear differential equations," Differential Equations, vol.14, pp. 1483-1485, 1979.
- [6] A. Ievkovich, E. Zeheb and N. Cohen, "Frequency Response Envelopes of a Family of uncertain Continuous-Time Systems," IEEE Trans. CAS-I, Vol.42, no.3, pp.156-165, 1995.